



EV318282225

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 09/930,787  
Filing Date ..... August 14, 2001  
Inventor ..... Luan Tran  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2814  
Examiner ..... Howard Weiss  
Attorney's Docket No. .... MI22-1784  
Title: Memory Integrated Circuitry

**RESPONSE TO APRIL 14, 2003 OFFICE ACTION**

To: Mail Stop Fee Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

From: D. Brent Kenady  
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Responsive to the Office Action dated April 14, 2003, Applicant amends  
and remarks as follows:

**AMENDMENTS**

Underlines indicate insertions and ~~strikeouts~~ indicate deletions.